

## T496C226M010ATE2K0

T496, Tantalum, MnO2 Tantalum, Fused, 22 uF, 20%, 10 VDC, SMD, MnO2, Molded, Fused, N/A, 2 Ohms, 6032, 2.8 mm, 1.3 mm

CATHODE (-) END VIEW



ANODE (+) END VIEW

Dimensions

L

W

н

Т s

F

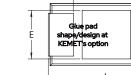
А

В

Е G

B • S ----- |- s-— G -BOTTOM VIEW ٠A

SIDE VIEW



6mm +/-0.3mm

3.2mm +/-0.3mm 2.5mm +/-0.3mm

1.3mm +/-0.3mm

2.2mm +/-0.1mm

0.5mm +/-0.15mm

0.13mm REF

3.1mm MIN

2.4mm REF

2.8mm REF

Click here for the 3D model.

General Information	
Series	T496
Dielectric	MnO2 Tantalum
Style	SMD Chip
Description	SMD, MnO2, Molded, Fused
Features	Integral Fuse
RoHS	Yes
Termination	Tin
AEC-Q200	No
Typical Component Weight	224.48 mg
Shelf Life	156 Weeks
MSL	1

Specifications	
Capacitance	22 uF
Tolerance	20%
Voltage DC	10 VDC (85C), 6.7 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	85°C
Dissipation Factor	6% 120Hz 25C
Failure Rate	N/A
ESR	2000 mOhms (100kHz 25C)
Ripple Current	235 mA (rms, 100kHz 25C), 211.5 mA (rms, 85C), 94 mA (rms, 125C)
Leakage Current	2.2 uA (5min 25°C)

Р	0.9mm REF
R	1mm REF
Х	0.1mm +/-0.1mm REF
Packaging Specifications	
Packaging	T&R, 178mm

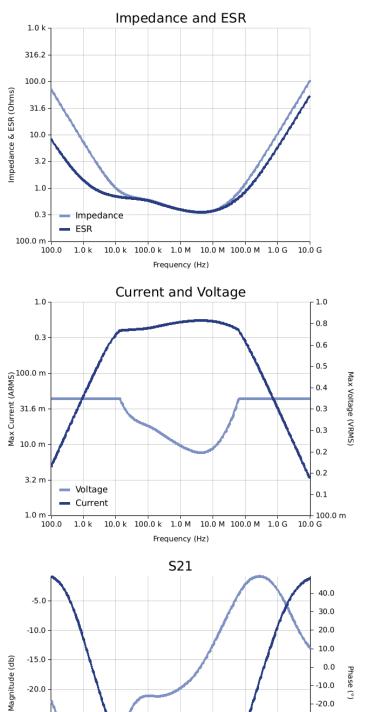
Packaging Specifications	
Packaging	T&R, 178mm
Packaging Quantity	500

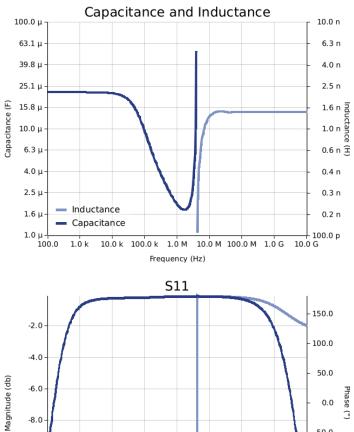
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## Simulations

For the complete simulation environment please visit K-SIM.





10.0 k 100.0 k 1.0 M 10.0 M 100.0 M 1.0 G

Frequency (Hz)

-10.0

-12.0

Phase -10.0

°

-20.0

-30.0 -40.0

-50.0

-60.0

10.0 G

100.0

S11 Phase

1.0 k

S11 Magnitude

-

100.0

S21 Phase S21 Magnitude

10.0 k 100.0 k

1.0 M 10.0 M 100.0 M 1.0 G

Frequency (Hz)

1.0 k

-20.0

-25.0

-30.0

-35.0

-50.0

-100.0

-150.0

10.0 G



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## These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
  The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
  The effects shown herein are based on measured data from a multiple part sample of the parts in question.
  Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
  The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages and the part of the parts of the part of the
- generated at any other harmonics.
  Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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