

LDBCC2680JC5N0

Aliases (DBCC2680JC5N0)

LDB, Film, Metallized PPS Stacked, General Purpose, 0.068 uF, 5%, 50 VDC, 125°C, 1812, 1.7 mm



Click [here](#) for the 3D model.

General Information

| | |
|--------------------------|------------------------|
| Series | LDB |
| Dielectric | Metallized PPS Stacked |
| Style | SMD Chip |
| RoHS | Yes |
| AEC-Q200 | No |
| Typical Component Weight | 0.2 g |
| Shelf Life | 104 Weeks |
| MSL | 3 |

Dimensions

| | |
|-----------|-------------------|
| Chip Size | 1812 |
| L | 4.7mm +0.3/-0.2mm |
| W | 3.3mm +/-0.3mm |
| T | 1.7mm MAX |
| B | 0.4mm +0.5/-0.3mm |

Packaging Specifications

| | |
|--------------------|------|
| Packaging | T&R |
| Packaging Quantity | 4000 |

Specifications

| | |
|-----------------------|------------|
| Capacitance | 0.068 uF |
| Tolerance | 5% |
| Voltage DC | 50 VDC |
| Temperature Range | -55/+125°C |
| Rated Temperature | 125°C |
| Dissipation Factor | 0.6% 1kHz |
| Insulation Resistance | 3 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.