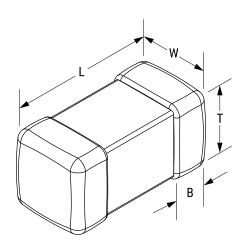


L0805CR10JRMST

Aliases (L0805CR10JRMST)

Obsolete

KEMET, L-RMS, Ceramic, 5%, 100 nH, 0805



General Information	
Series	L-RMS
Style	SMD Multi-Layer
Core	Ceramic
Description	Surface Mount Inductor
RoHS	Yes

Click here for the 3D model.

Dimensions	
Chip Size	0805
L	2mm +0.3/-0.1mm
W	1.25mm +/-0.2mm
Т	1mm +0.2/-0.3mm
Packaging Specifications	
Packaging	T&R
Packaging Quantity	3000

Specifications	
Inductance	0.1 uH (100 MHz)
Inductance Tolerance	5%
Inductance	100 nH
Rated Current	300 mAmps (MAX)
Temperature Range	-40/+85°C
DC Resistance	900 mOhms
DC Resistance	0.9 Ohms
Quality Factor	23 at 100 MHz, 37 at 500 MHz
Self-Resonant Frequency	600 MHz

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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