

LO805C5N6SRMST

Aliases (LO805C5N6SRMST)

Obsolete

KEMET, L-RMS, Ceramic, +/-0.3 nH, 5,600 pH, 0805



Click [here](#) for the 3D model.

General Information

Series	L-RMS
Style	SMD Multi-Layer
Core	Ceramic
Description	Surface Mount Inductor
RoHS	Yes

Dimensions

Chip Size	0805
L	2mm +0.3/-0.1mm
W	1.25mm +/-0.2mm
T	0.85mm +/-0.2mm

Packaging Specifications

Packaging	T&R
Packaging Quantity	4000

Specifications

Inductance	0.0056 uH (100 MHz)
Inductance Tolerance	+/-0.3 nH
Inductance	5,600 pH
Rated Current	300 mAmps (MAX)
Temperature Range	-40/+85°C
DC Resistance	230 mOhms
DC Resistance	0.23 Ohms
Quality Factor	20 at 100 MHz, 51 at 500 MHz, 60 at 800 MHz, 66 at 1000 MHz
Self-Resonant Frequency	3200 MHz

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