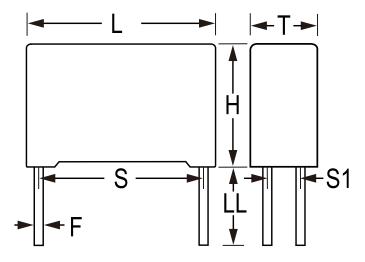


General Information

Series



C4AF, Film, Metallized Polypropylene, Power, 15 uF, 10%, 500 VDC, 250 VAC, 85°C, 37.5 mm



Dielectric Metallized Polypropylene Style Radial Harsh Environmental Conditions Features RoHS Yes 4 Wire Leads Lead AEC-Q200, IEC61071, EN61071, VDE0560 Qualifications AEC-Q200 Yes 74.9 g **Typical Component Weight** Miscellaneous Rth = 15 C/W.

13 nH (ESL)

C4AF

42mm +0.6/-0.7mm Capacitance 15 uF	
45mm +0.2/-0.7mm Tolerance 10%	
30mm +0.4/-0.7mm Voltage DC 500 VDC	
37.5mm +/-0.4mm Voltage AC 250 VAC	
20.3mm +/-0.4mm Temperature Range -55/+105°C	
6mm +0/-2mm Rated Temperature 85°C	
1.2mm +/-0.05mm Insulation Resistance 2 GOhms	
Max dV/dt 27 V/us	
ESR 3.4 mOhms	
Bulk, Box Ripple Current 20.8 Amps Irms (MA)	X), 400
36 Amps lpkr	

Inductance

Click here for the 3D model.

т	30mm +0.4/-0.7mm
S	37.5mm +/-0.4mm
S1	20.3mm +/-0.4mm
LL	6mm +0/-2mm
F	1.2mm +/-0.05mm
Packaging Specifications	

Packaging Specifications	
Packaging	

Dimensions

L н

Fackaging	DUIK, DOX
Packaging Quantity	36

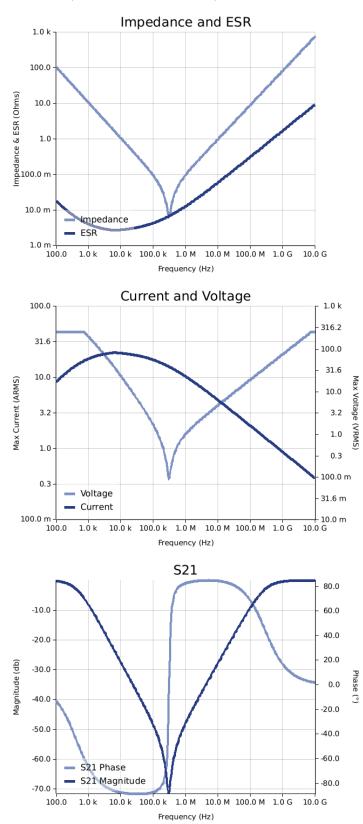
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and
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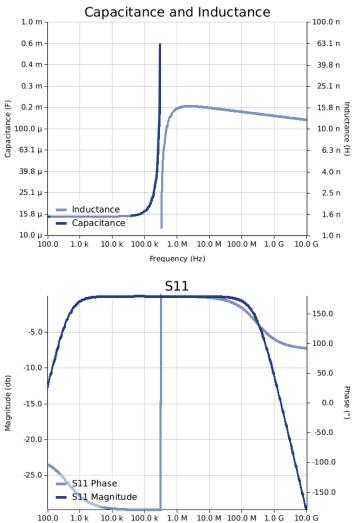


C4AF1BW515OT3LK C4AF, Film, Metallized Polypropylene, Power, 15 uF, 10%, 500 VDC, 250 VAC, 85°C, 37.5 mm

Simulations

For the complete simulation environment please visit K-SIM.





Frequency (Hz)



These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages and the part of the parts of the part of the
- generated at any other harmonics.
 Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.