

C1812T103J1GALTU

Aliases (C1812T103J1GAL7800)

SMD COTS COG, Ceramic, 0.01 uF, 5%, 100 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1812, 2.3 mm



Click [here](#) for the 3D model.

General Information

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| Series | SMD COTS COG |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I |
| Features | Ultra-Stable, Low Loss, Class I |
| RoHS | No |
| Prop 65 | WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov/ |
| SCIP Number | 2d771165-5336-48a3-96fa-3663929fd828 |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8% |
| AEC-Q200 | No |
| Typical Component Weight | 67 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

Dimensions

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|-----------|-----------------|
| Chip Size | 1812 |
| L | 4.5mm +/-0.3mm |
| W | 3.2mm +/-0.3mm |
| T | 1.4mm +/-0.15mm |
| S | 2.3mm MIN |
| B | 0.6mm +/-0.35mm |

Packaging Specifications

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|--------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 1000 |

Specifications

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|--|------------------------|
| Capacitance | 0.01 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 5% |
| Voltage DC | 100 VDC |
| Dielectric Withstanding Voltage | 250 VDC |
| Temperature Range | -55/+125°C |
| Temp. Coefficient | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1kHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 kHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 100 GOhms |

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