

## C1812J105K1RACTU

Aliases (C1812J105K1RAC7800)

SMD Comm X7R FO, Ceramic, 1uF, 10%, 100 VDC, X7R, SMD, MLCC, Open Mode, Temperature Stable, 1812



Click [here](#) for the 3D model.

### General Information

Series	SMD Comm X7R FO
Style	SMD Chip
Description	SMD, MLCC, Open Mode, Temperature Stable
Features	Open Mode, Temperature Stable
RoHS	Yes
Termination	Flexible Termination
Marking	No
AEC-Q200	No
Typical Component Weight	185 mg
Shelf Life	78 Weeks
MSL	1

### Dimensions

Chip Size	1812
L	4.5mm +/-0.4mm
W	3.2mm +/-0.3mm
T	2mm +/-0.20mm
S	2.3mm MIN
B	0.7mm +/-0.35mm

### Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	500

### Specifications

Capacitance	1uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	1 GOhms

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