

C1812C562JBGACTU

Specifications

Insulation Resistance

Aliases (C1812C562JBGAC7800) SMD Comm COG HV, Ceramic, 5,600 pF, 5%, 630 VDC, COG, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 1812, 2.3 mm



General Information	
Series	SMD Comm COG HV
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	87 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
Т	1.6mm +/-0.20mm
S	2.3mm MIN
В	0.6mm +/-0.35mm

Capacitance	5,600 pF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	630 VDC
Dielectric Withstanding Voltage	945 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour

100 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

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