

C1812C561JGGACTU

Aliases (C1812C561JGGAC7800)

SMD Comm COG HV, Ceramic, 560 pF, 5%, 2,000 VDC, COG, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 1812, 2.3 mm



Click [here](#) for the 3D model.

General Information

Series	SMD Comm COG HV
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	67 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	560 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	2000 VDC
Dielectric Withstanding Voltage	2,400 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

Dimensions

Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	1.4mm +/-0.15mm
S	2.3mm MIN
B	0.6mm +/-0.35mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

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