

C1210T222K2GCLTU

Aliases (C1210T222K2GCL7800) SMD COTS COG, Ceramic, 2,200 pF, 10%, 200 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1210, 1.5 mm



Click here for the 3D model.

General Information	
Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov /
SCIP Number	2d771165-5336-48a3-96fa-366 3929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	40 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1210
L	3.2mm +/-0.2mm
W	2.5mm +/-0.2mm
т	1.25mm +/-0.15mm
S	1.5mm MIN
В	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2500

Specifications	
Capacitance	2,200 pF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	200 VDC
Dielectric Withstanding Voltage	500 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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