

## C1210C275K4PACTU

**General Information** 

**Specifications** 

Insulation Resistance

Aliases (C1210C275K4PAC7800) SMD Comm X5R, Ceramic, 2.7 uF, 10%, 16 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1210, 1.5 mm



Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	65 mg
Shelf Life	78 Weeks
MSL	1

1210
3.2mm +/-0.2mm
2.5mm +/-0.2mm
1.25mm +/-0.15mm
1.5mm MIN
0.5mm +/-0.25mm

Capacitance	2.7 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	16 VDC
Dielectric Withstanding Voltage	40 VDC
Temperature Range	-55/+85°C
Temp. Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1 kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee

Time is 1000 Hours

37 MOhms

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2500

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