

C1210C273KARACTU

Aliases (C1210C273KARAC7800) SMD Comm X7R, Ceramic, 0.027 uF, 10%, 250 VDC, X7R, SMD, MLCC, Temperature Stable, Class II, 1210, 1.5 mm



Click here for the 3D model.

General Information	
Series	SMD Comm X7R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	40 mg
Shelf Life	78 Weeks
MSL	1

Dimensions		Specifications
Chip Size	1210	Capacitance
L	3.2mm +/-0.2mm	Measurement Condi
W	2.5mm +/-0.2mm	Tolerance
т	0.78mm +/-0.10mm	Voltage DC
S	1.5mm MIN	Dielectric Withstand
В	0.5mm +/-0.25mm	Temperature Range
		Temp. Coefficient
Packaging Specifications		Canacitance Change

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

Specifications	
Capacitance	0.027 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	250 VDC
Dielectric Withstanding Voltage	625 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5%1kHz1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	37.037 GOhms

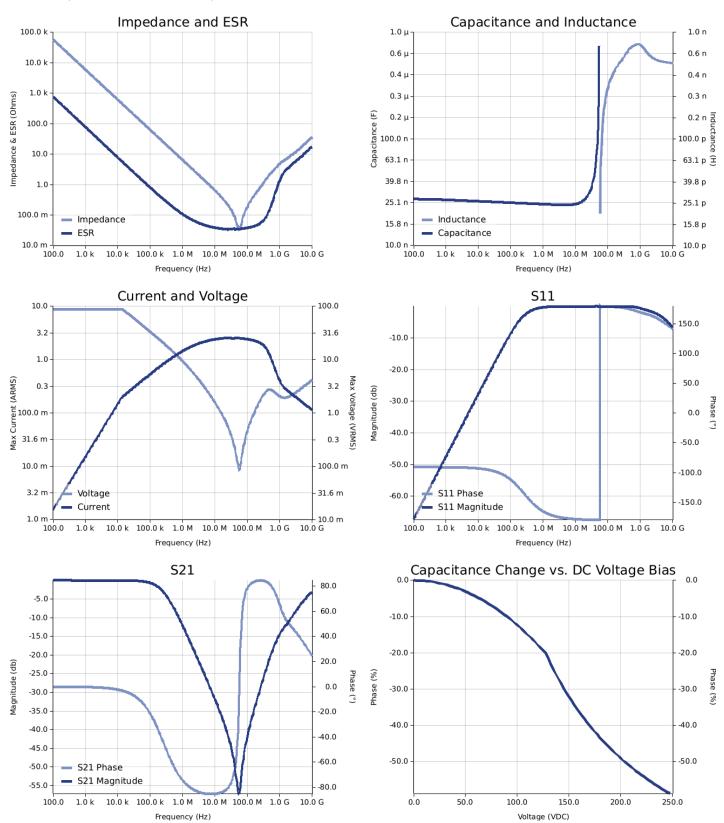
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Simulations

For the complete simulation environment please visit K-SIM.





Temperature Stable, Class II, 1210, 1.5 mm

These are simulations. This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages are not combined with voltages.
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.