

C1206T333JARCLTM

Aliases (C1206T333JARCL7025)

SMD COTS X7R, Ceramic, 0.033 uF, 5%, 250 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1206, 1.5 mm



Click [here](#) for the 3D model.

General Information

Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	No
Prop 65	WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov/
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	Yes
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	17 mg
Shelf Life	78 Weeks
MSL	1

Dimensions

Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
T	0.78mm +/-0.10mm
S	1.5mm MIN
B	0.5mm +/-0.25mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

Specifications

Capacitance	0.033 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	250 VDC
Dielectric Withstanding Voltage	625 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	30.303 GOhms

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