

## C1206T105J5RALTU

Aliases (C1206T105J5RAL7800)

SMD COTS X7R, Ceramic, 1 uF, 5%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1206, 1.5 mm



Click [here](#) for the 3D model.

### General Information

Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	No
Prop 65	<b>WARNING:</b> Cancer and reproductive harm - <a href="https://www.p65warnings.ca.gov/">https://www.p65warnings.ca.gov/</a>
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	No
Typical Component Weight	25 mg
Shelf Life	78 Weeks
MSL	1

### Dimensions

Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
T	1mm +/-0.10mm
S	1.5mm MIN
B	0.5mm +/-0.25mm

### Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2500

### Specifications

Capacitance	1 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	500 MOhms

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