

C1206C102JAGECTU

Aliases (C1206C102JAGEC7800)

ESD SMD Comm COG, Ceramic, 1,000 pF, 5%, 250 VDC, COG, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 1206, 1.5 mm



General Information	
Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	15 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
Т	0.78mm +/-0.10mm
S	1.5mm MIN
В	0.5mm +/-0.25mm

W	1.6mm +/-0.2mm	Tolerance	5%
Т	0.78mm +/-0.10mm	Voltage DC	250 VDC
S	1.5mm MIN	ESD Level per AEC-Q200	12,000 V ESD Level
В	0.5mm +/-0.25mm	Dielectric Withstanding Voltage	625 VDC
		Temperature Range	-55/+125°C
Packaging Specifications		Temp. Coefficient	COG
Packaging Specifications Packaging	T&R, 180mm, Plastic Tape	Temp. Coefficient Capacitance Change with	COG 30 ppm/C, 1MegaHz 1.0Vrms
	T&R, 180mm, Plastic Tape	•	
Packaging		Capacitance Change with Reference to +25°C and 0 VDC	

Specifications

Capacitance	1,000 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	250 VDC
ESD Level per AEC-Q200	12,000 V ESD Level
Dielectric Withstanding Voltage	625 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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