

## C0805T391F5GCLTU

Aliases (C0805T391F5GCL7800) SMD COTS COG, Ceramic, 390 pF, 1%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805, 0.7 mm



Click here for the 3D model.

SeriesSMD COTS COGStyleSMD ChipDescriptionSMD, MLCC, COTS, Ultra-Stable, Low Loss, Class IFeaturesUltra-Stable, Low Loss, Class IRoHSNoProp 65WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov /SCIP Number2d771165-5336-48a3-96fa-366 3929fd828TerminationLead (SnPb)MarkingNoFailure RateTesting per MIL-PRF-55681 PDA 8% DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition AAEC-Q200NoTypical Component Weight11 mgShelf Life78 WeeksMSL1	General Information	
PescriptionSMD, MLCC, COTS, Ultra-Stable, Low Loss, Class IFeaturesUltra-Stable, Low Loss, Class IRoHSNoProp 65WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov //SCIP Number2d771165-5336-48a3-96fa-366 3929fd828TerminationLead (SnPb)MarkingNoFailure RateTesting per MIL-PRF-55681 PDA 8% DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition AAEC-Q200NoTypical Component Weight11 mgShelf Life78 Weeks	Series	SMD COTS COG
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Typical Component Weight 11 mg   Shelf Life 78 Weeks	Failure Rate	8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103,
Shelf Life 78 Weeks	AEC-Q200	No
	Typical Component Weight	11 mg
MSL 1	Shelf Life	78 Weeks
	MSL	1

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
т	0.78mm +/-0.10mm
S	0.7mm MIN
В	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	390 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	1%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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