

C0805T221J5GCLTU

Aliases (C0805T221J5GCL7800) SMD COTS COG, Ceramic, 220 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805, 0.7 mm



| General Information | |
|--------------------------|---|
| Series | SMD COTS COG |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I |
| Features | Ultra-Stable, Low Loss, Class I |
| RoHS | No |
| Prop 65 | WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov / |
| SCIP Number | 2d771165-5336-48a3-96fa-366 3929fd828 |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200 | No |
| Typical Component Weight | 11 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Dimensions | |
|--------------------------|------------------|
| Chip Size | 0805 |
| L | 2mm +/-0.2mm |
| W | 1.25mm +/-0.2mm |
| T | 0.78mm +/-0.10mm |
| S | 0.7mm MIN |
| В | 0.5mm +/-0.25mm |
| | |
| Packaging Specifications | |

4000

Packaging

Packaging Quantity

T&R, 180mm, Paper Tape

| Specifications | |
|--|---------------------------|
| Capacitance | 220 pF |
| Measurement Condition | 1 MHz 1.0Vrms |
| Tolerance | 5% |
| Voltage DC | 50 VDC |
| Dielectric Withstanding Voltage | 125 VDC |
| Temperature Range | -55/+125°C |
| Temp. Coefficient | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MegaHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 MHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 100 GOhms |

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