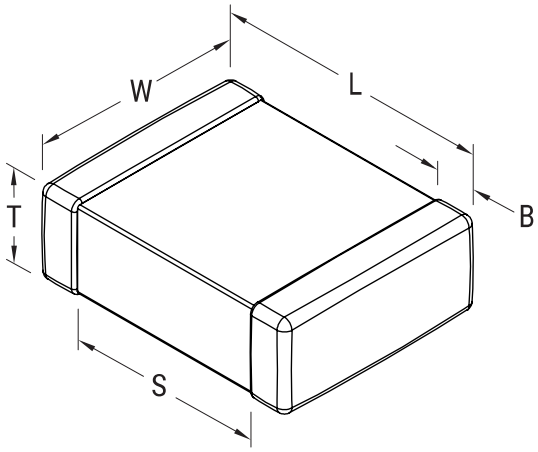
C0805T109C5GCLTU

Aliases (C0805T109C5GCL7800)

SMD COTS C0G, Ceramic, 1 pF, +/-0.25 pF, 50 VDC, C0G, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805, 0.7 mm



Click here for the 3D model.

General Information	
Series	SMD COTS C0G
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	**WARNING:** Cancer and reproductive harm - https://www.p65warnings.ca.gov/ .
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.78mm +/-0.10mm
S	0.7mm MIN
B	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	1 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	+/-0.25 pF
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	C0G
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms