

C0805T103K2RCLTU

Aliases (C0805T103K2RCL7800) SMD COTS X7R, Ceramic, 0.01 uF, 10%, 200 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0805, 0.7 mm



Click here for the 3D model.

| General Information | |
|--------------------------|---|
| Series | SMD COTS X7R |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | No |
| Prop 65 | WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov / |
| SCIP Number | 2d771165-5336-48a3-96fa-366 3929fd828 |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200 | No |
| Typical Component Weight | 11 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Dimensions | |
|------------|------------------|
| Chip Size | 0805 |
| L | 2mm +/-0.2mm |
| W | 1.25mm +/-0.2mm |
| т | 0.78mm +/-0.10mm |
| S | 0.7mm MIN |
| В | 0.5mm +/-0.25mm |
| | |

| Packaging Specifications | |
|--------------------------|------------------------|
| Packaging | T&R, 180mm, Paper Tape |
| Packaging Quantity | 4000 |
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| Specifications | |
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| Capacitance | 0.01 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 10% |
| Voltage DC | 200 VDC |
| Dielectric Withstanding Voltage | 500 VDC |
| Temperature Range | -55/+125°C |
| Temp. Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 2.5%1kHz1.0Vrms |
| Aging Rate | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 100 GOhms |

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