

C0805C132J2TACTU

Aliases (C0805C132J2TAC7800) SMD Comm X8G HT150C, Ceramic, 1,300 pF, 5%, 200 VDC, X8G, SMD, MLCC, High Temperature, Ultra-Stable, 0805, 0.7 mm



Click here for the 3D model.

4000

| General Information | |
|--------------------------|--|
| Series | SMD Comm X8G HT150C |
| Style | SMD Chip |
| Description | SMD, MLCC, High Temperature, Ultra-Stable |
| Features | High Temperature, Ultra-Stable |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| AEC-Q200 | No |
| Typical Component Weight | 11 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| | | Specifications |
|----------|------------------------|---------------------------------|
| | 0805 | Capacitance |
| | 2mm +/-0.2mm | Measurement Condition |
| | 1.25mm +/-0.2mm | Tolerance |
| | 0.78mm +/-0.10mm | Voltage DC |
| | 0.7mm MIN | Dielectric Withstanding Voltage |
| | 0.5mm +/-0.25mm | Temperature Range |
| | | Temp. Coefficient |
| ications | | Capacitance Change with |
| | T&R, 180mm, Paper Tape | Reference to +25°C and 0 VDC |

| Specifications | |
|--|--|
| Capacitance | 1,300 pF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 5% |
| Voltage DC | 200 VDC |
| Dielectric Withstanding Voltage | 500 VDC |
| Temperature Range | -55/+150°C |
| Temp. Coefficient | X8G |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1kHz 1.0Vrms |
| Dissipation Factor | 0.1% 1 kHz 1.0Vrms |
| Aging Rate | 0% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 100 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

Dimensions Chip Size

Packaging Specific

Packaging Quantity

Packaging

L W T S B