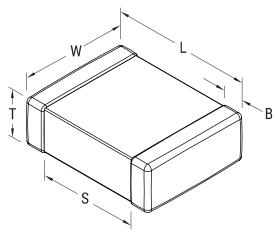


C0603T560F5GCLTU

Aliases (C0603T560F5GCL7867) SMD COTS C0G, Ceramic, 56 pF, 1%, 50 VDC, C0G, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



Click here for the 3D model.

General Information	
Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov /
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	3.7 mg
Shelf Life	78 Weeks
MSL	1

0603
1.6mm +/-0.15mm
0.8mm +/-0.15mm
0.8mm +/-0.07mm
0.5mm MIN
0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	56 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	1%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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