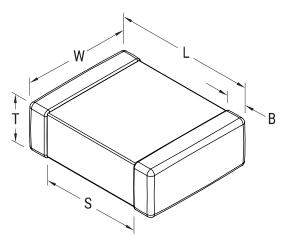


## C0603T220J5GCLTU

Aliases (C0603T220J5GCL7867) SMD COTS C0G, Ceramic, 22 pF, 5%, 50 VDC, C0G, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



Click here for the 3D model.

COTS COG Chip MLCC, COTS, Ultra-Stable, Loss, Class I A-Stable, Low Loss, Class I
), MLCC, COTS, Ultra-Stable, Loss, Class I a-Stable, Low Loss, Class I
Loss, Class I a-Stable, Low Loss, Class I
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d (SnPb)
ing per MIL-PRF-55681 PDA DPA per EIA-469, Humidity MIL-STD-202, Method 103, dition A
ng
Veeks

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	22 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.