

C0603T102J5GCLTU

Aliases (C0603T102J5GCL7867) SMD COTS COG, Ceramic, 1,000 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



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COTS COG Chip MLCC, COTS, Ultra-Stable, Loss, Class I A-Stable, Low Loss, Class I
), MLCC, COTS, Ultra-Stable, Loss, Class I a-Stable, Low Loss, Class I
Loss, Class I a-Stable, Low Loss, Class I
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oductive harm - s://www.p65warnings.ca.gov
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d (SnPb)
ing per MIL-PRF-55681 PDA DPA per EIA-469, Humidity MIL-STD-202, Method 103, dition A
ng
Veeks

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	1,000 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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