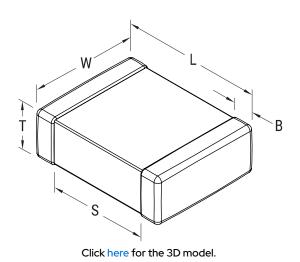


C0603T100K5GCLTU

Aliases (C0603T100K5GCL7867) SMD COTS C0G, Ceramic, 10 pF, 10%, 50 VDC, C0G, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



SMD COTS COG
SMD Chip
SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Ultra-Stable, Low Loss, Class I
No
WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov /
2d771165-5336-48a3-96fa-366 3929fd828
Lead (SnPb)
No
Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
No
3.7 mg
78 Weeks
1

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm
Packaging Specifications	

4000

Packaging

Packaging Quantity

T&R, 180mm, Paper Tape

Specifications	
Capacitance	10 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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