

C0402C182J5JACTU

Specifications

Insulation Resistance

Aliases (C0402C182J5JAC7867) SMD Comm U2J, Ceramic, 1,800 pF, 5%, 50 VDC, U2J, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 0402, 0.3 mm



General Information	
Series	SMD Comm U2J
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	1.06 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0402
L	1mm +/-0.05mm
W	0.5mm +/-0.05mm
Т	0.5mm +/-0.05mm
S	0.3mm MIN
В	0.3mm +/-0.1mm

Capacitance	1,800 pF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	U2J
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	-750+/-120 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0.1% Loss/Decade Hour: Referee Time is 1000 Hours
Reference to +25°C and 0 VDC Applied (TCC) Dissipation Factor	1.0Vrms 0.1% 1 kHz 1.0Vrms 0.1% Loss/Decade Hour: Referee

100 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	10000

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